

AMENDMENTS TO THE ABSTRACT

Please amend the Abstract as follows:

ABSTRACT

The present drive circuit (1) includes: a first N-type transistor (10) connected between a power supply potential (~~VDD~~) line and an output node (N2); a P-type transistor (8) connected between the power supply potential (~~VDD~~) line and the gate of the first N-type transistor (10); a second N-type transistor (9) forming a diode connected between the gate of the first N-type transistor (10) and a prescribed node (N9); and a differential amplifier (2) for regulating the gate potential of the P-type transistor (8) to match the potential (~~VM~~) at the prescribed node (9) with the input potential (~~VI~~).